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09/752538
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Class	Subcla	ISSUE CLASSIFICAT

U.S. UTILITY Patent Application

HKM O.I.P.E. SCANNED <i>[Signature]</i> Q.A. <i>TC</i>	PATENT DATE
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APPLICATION NO. 09/752538	CONT/PRIOR	CLASS 174	SUBCLASS 262	ART UNIT 284 2827	EXAMINER CUNEO ^I PATEL
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Stephen Ohr

Layered circuit boards and methods of production thereof

PTO-2040
12/88[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
	_____ _____ (Primary Examiner) _____ (Date)			ISSUE FEE	
				Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	

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